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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/555,266	11/01/2005	Peter Fuhrmann	DE 030145	9354

65913 7590 10/30/2008
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EXAMINER

BARON, HENRY

ART UNIT	PAPER NUMBER
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2416

NOTIFICATION DATE	DELIVERY MODE
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10/30/2008

ELECTRONIC

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

ip.department.us@nxp.com

Office Action Summary	Application No. 10/555,266	Applicant(s) FUHRMANN ET AL.	
	Examiner HENRY BARON	Art Unit 2416	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 24 June 2008.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-16 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-16 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

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Detailed Action

ERROR DETECTION AND SUPPRESSION IN A TDMA-BASED NETWORK NODE

Response to Arguments/Remarks

1. Claims 1 – 16 are pending in the application. Claims 1 - 11 are amended with claims 12 – 16 new.
2. Applicant's arguments filed 06/24/2008 have been fully considered but moot in view of new grounds for rejection.

Specification

3. The following guidelines illustrate the preferred layout for the specification of a utility application. These guidelines are suggested for the applicant's use.

Arrangement of the Specification

4. As provided in 37 CFR 1.77(b), the specification of a utility application should include the following sections in order. Each of the lettered items should appear in upper case, without underlining or bold type, as a section heading. If no text follows the section heading, the phrase "Not Applicable" should follow the section heading:

- (a) TITLE OF THE INVENTION.
- (b) CROSS-REFERENCE TO RELATED APPLICATIONS.
- (c) STATEMENT REGARDING FEDERALLY SPONSORED RESEARCH OR DEVELOPMENT.
- (d) THE NAMES OF THE PARTIES TO A JOINT RESEARCH AGREEMENT.
- (e) INCORPORATION-BY-REFERENCE OF MATERIAL SUBMITTED ON A COMPACT DISC.
- (f) BACKGROUND OF THE INVENTION.
 - (1) Field of the Invention.
 - (2) Description of Related Art including information disclosed under 37 CFR 1.97 and 1.98.
- (g) BRIEF SUMMARY OF THE INVENTION.
- (h) BRIEF DESCRIPTION OF THE SEVERAL VIEWS OF THE DRAWING(S).
- (i) DETAILED DESCRIPTION OF THE INVENTION.
- (j) CLAIM OR CLAIMS (commencing on a separate sheet).

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(k) ABSTRACT OF THE DISCLOSURE (commencing on a separate sheet).

(l) SEQUENCE LISTING (See MPEP § 2424 and 37 CFR 1.821-1.825. A "Sequence Listing" is required on paper if the application discloses a nucleotide or amino acid sequence as defined in 37 CFR 1.821(a) and if the required "Sequence Listing" is not submitted as an electronic document on compact disc).

Claim Rejections - 35 USC § 103

5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

a. A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

6. Claims 1 and 11 – 12 are rejected under 35 U.S.C. 103 (a) as being unpatentable over Belschner, et al (U.S. Patent 7103805), in view of Kleveland (U.S. Patent 5528168)

7. With regards to claims 1 and 12, Belschner teaches of a network node comprising a communication unit for the implementation of a communication protocol for communication with other network nodes via a communication medium a bus monitor, and a bus driver, where the communication unit and the bus monitor (2: [0046] read The bus monitor unit which is integrated into the central node is suitable for monitoring access of users to the data bus, without having to install the bus monitor unit in a decentralized controller for this purpose. The central bus monitor unit i.e. bus monitor unit and the diagnostic unit i.e. communication unit can be used to prevent faulty access to the data bus by a user) each mutually independently; 3: [0030] read [t]he central node with the integrated diagnostic unit therefore forms a closed system which preferably also has fault-handling routines, so that the central node is operationally capable independently of external diagnostic units, and has its own fault detection means i.e. mutually independent.); each implement an access time schedule contained in a configuration data record (2: [0013] read For this purpose, a time-registering means i.e. access time schedule is provided which registers the time patterns of the data bus for the transmission of a user i.e. configuration data record and, triggered by these time patterns, assigns a transmission slot to each user.) and each make

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available, in accordance with the access time schedule, a release signal for the bus driver the bus driver evaluates these two release signals (3: [0013] read Based on the second time pattern made available to it, the diagnostic unit checks whether the bus monitor unit regularly retriggers in response to the time patterns by means of the trigger signals i.e. evaluates these two release signals).

8. However Belschner does not disclose in the event that the two release signals do not coincide of blocking the access of the network node to the communication medium

9. Kleveland teaches this limitation (10: [0030] read [a]rbitration logic 510 includes a temporary bus master identification register, master ID 520, coupled by master comparison bits 525 to comparison circuit 540. i.e. logic for blocking the access of the network node if two release signals do not coincide)

10. It would have been obvious at the time the invention was made to a person of ordinary skill in the art to modify the bus access teachings of Belschner with the comparison circuit teachings of Kleveland.

11. In this manner, access to data bus can be regulated in a discipline manner so as to mitigate user collisions on the data bus which would yield the system dysfunctional.

12. In regards to claim 11, Belschner teaches of a bus driver for a network node which is provided for communication with other network nodes via a communication medium and a bus driver that evaluates two mutually independent release signals that implement an access time schedule to generate the release signals. (2: [0046] read The bus monitor unit which is integrated into the central node is suitable for monitoring access of users to the data bus, without having to install the bus monitor unit in a decentralized controller for this purpose. The central bus monitor unit i.e. bus monitor unit and the diagnostic unit i.e. communication unit can be used to prevent faulty access to the data bus by a user) each mutually independently; (5: [0006] read [t]he watchdog i.e. bus driver, monitors the cyclical synchronization of the bus monitor unit with the time patterns of the data bus i.e. evaluates two release signals for equality of the release information made available to it by two separate units for a communication medium, and switches the bus monitor unit to an inactive state when the trigger signal

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fails to occur, blocking or releasing the communication in a way which can be configured for all the users i.e. in the event that the release signals do not coincide, the bus driver blocks the access of the network node. And 3: [0030] read [t]he central node with the integrated diagnostic unit therefore forms a closed system which preferably also has fault-handling routines, so that the central node is operationally capable independently of external diagnostic units, and has its own fault detection means i.e. mutually independent.); and 2: [0013] read For this purpose, a time-registering means i.e. access time schedule is provided which registers the time patterns of the data bus for the transmission of a user i.e. configuration data record and, triggered by these time patterns, assigns a transmission slot to each user.) and each make available, in accordance with the access time schedule, a release signal for the bus driver the bus driver evaluates these two release signals and 3: [0013] read based on the second time pattern made available to it, the diagnostic unit checks whether the bus monitor unit regularly retriggers in response to the time patterns by means of the trigger signals i.e. evaluates these two release signals)

13. However, Belschner does not disclose where the bus driver evaluates two release signals for equality of the release information made available to it by two separate units.

14. Kleveland teaches this limitation (10: [0030] read [a]rbitration logic 510 includes a temporary bus master identification register, master ID 520, coupled by master comparison bits 525 to comparison circuit 540. i.e. logic for blocking the access of the network node if two release signals do not coincide or are not equal.)

15. It would have been obvious at the time the invention was made to a person of ordinary skill in the art to modify the bus access teachings of Belschner with the comparison circuit teachings of Kleveland.

16. In this manner, access to data bus can be regulated in a discipline manner so as to mitigate user collisions on the data bus which would yield the system dysfunctional.

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17. Claims 3 – 11, 13, and 15 – 16 are rejected under 35 U.S.C. 103 (a) as being unpatentable over Belschner, et al (U.S. Patent 7103805), in view of Kleveland (U.S. Patent 5528168) and in further view of Riley et al (U.S. Patent 5706289).

18. With regards to claims 3 and 15, Belschner, modified teaches of a network node as claimed in claim 1, characterized in that the release signals of the communication unit and the bus monitor, but does not teach that the signals are coded inversely to one another.

19. The polarities of signals in VLSI are determined by the physical design, timing and performance specification of the logic as shown in, for example, in Riley Figure 13 element 445 and 446.

20. It would have been obvious at the time the invention was made by a person of to having ordinary skill in the art to modify the teachings of Belschner, modified Riley so that release signals are coded inversely to one another if the design so dictated.

21. In this manner, the time slot of bus could be blocked or not in the most expedient manner per a specific set of physical specifications.

22. In regards to claims 4 – 5 and 16, Belschner modified, teach a network node characterized in that the evaluation of the two release signals is undertaken in the bus driver, but does not teach the of the influence of a low-pass filter or of a low-pass filter of configurable design.

23. Riley teaches of the evaluation of the two signals is undertaken in the bus driver with the influence of a low-pass filter or of a low-pass filter of configurable design. (8: [0024] read shown in block form in FIG. 2A, the channel input signal at the channel A input terminal to the integrated circuit is fed through a dual signal conditioning circuit before further processing. The signal conditioning circuit includes a Channel A signal conditioning circuit (shown in FIG. 3A) i.e. configurable low-pass filter. As shown in FIG. 3A, the signal conditioning circuit 122 has an anti-aliasing filter, a hysteresis circuit, and a digital low pass filter.)

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24. It would have been obvious at the time the invention was made by a person of ordinary skill in the art to modify the teachings of Belschner, modified Riley to condition the input release signal with a configurable low pass filter.

25. In this manner, noise or channel transients can be mitigated thus improving the fidelity of the protection time slot logic for the bus.

26. In regards to claims 6 and 7, Belschner teaches that error-state detection generated in the bus driver is resettable from the outside and can be signaled to the outside. (4: [0056] read [t]he bus monitor unit is connected via an interface to a communications computer of the central node, which loads and calculates the time patterns i.e. access time schedule contained in a configuration data record, for the accepted transmission slots of the individual users i.e. communication nodes. The interface is a component of the configuration means).

27. Regarding claim 8, Belschner teaches that the bus monitor and the bus driver are integrated into one unit. (2: [0045] read the bus monitor unit is integrated into the central node is suitable for monitoring access of users to the data bus, without having to install the bus monitor unit in a decentralized controller for this purpose.).

28. In consideration of claim 9, Belschner teaches a network with network nodes where the network nodes communicate with each other via the communication medium. (Figure 1 element 6).

29. With regards to claim 10, Belschner teaches where redundant network channels are provided, wherein a bus monitor and a bus driver are assigned to each network channel in each network node (7: [0004] read FIG. 3 shows an example of a time pattern such as can be determined by the unit for setting the configuration parameters 21. First, two time slots are provided for the user 7, followed by a time slot for the third user 9. The two time slots which are represented in a hatched form are marked as blocked by the hatching, i.e. the bus monitor unit 5 has detected a faulty transmission signal at this time; as a result, the time slot is blocked both for transmission and reception. However, on the other hand, it would also be

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possible for signal filtering to take place so that the correct signal is generated by means of a filter or a redundant channel.).

30. In regards to claim 13, Belschner modified teaches the limitations of claim 12, but does not disclose where the bus driver evaluates the independently-generated release signals to ensure that both signals match one another to mitigate a network access condition resulting from an improperly-generated release signal.

31. Riley teaches where the bus driver evaluates the independently-generated release signals to ensure that both signals match one another to mitigate a network access condition resulting from an improperly-generated release signal. (6: [0061] read in mode one, during the first part of the time slot 65, data is placed on the data bus 46 by one or more input data link modules 32 and the data remains on the data bus 46 for the entire time slot i.e. both signals match one another. At the midpoint 64 of the time slot 65, the data on the data bus 46 is copied from the bus to output terminals 98 and 100 on at least one output data link module 32 for use by at least one output device 54. i.e. to mitigate a network access condition resulting from an improperly-generated release signal)

32. It would have been obvious at the time the invention was made by a person of to having ordinary skill in the art to modify the teachings of Belschner, modified Riley so that release signals are properly matched.

33. In this manner, the time slot of bus could be blocked and an improperly-generated release signal can be mitigated

34. Claims 2 and 14 are rejected under 35 U.S.C. 103(a) as being unpatentable over Belschner, et al (U.S. Patent 7103805), in view of in view of Kleveland (U.S. Patent 5528168) and in further view of Baek et al (U.S. Patent 5680554)

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35. In consideration of claims 2 and 14, Belschner, modified Riley teaches of a bus driver activating the transmission stage if there is no blockage of access to the communication medium present, but does not teach of transmission request signal to the bus driver.

36. Baek teaches of transmission request signal to the bus driver (4: [0054] read NRQ represents the basic unit of a data transmission request signal...)

37. It would have been obvious at the time the invention was made by a person of to having ordinary skill in the art to modify the teachings of Belschner, modified Riley with Baek.

38. In this manner, the bus driver will be enabled only when it has data to transmit thus minimizing collision with other bus drivers.

Conclusion

39. Any inquiry concerning this communication or earlier communications from the examiner should be directed to HENRY BARON whose telephone number is (571)270-1748. The examiner can normally be reached on 7:30 AM to 5:00 PM E.S.T. Monday to Friday.

40. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Seema Rao can be reached on (571) 272-3174. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

41. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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/H. B./
Examiner, Art Unit 2416
HB

/Brenda Pham/

Primary Examiner, Art Unit 2416